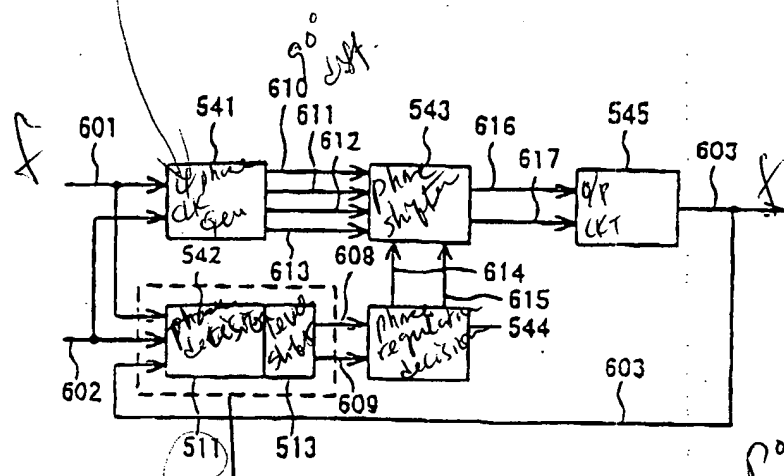


obj label in box

FIG. 1 PRIOR ART



★ 542 phase detector

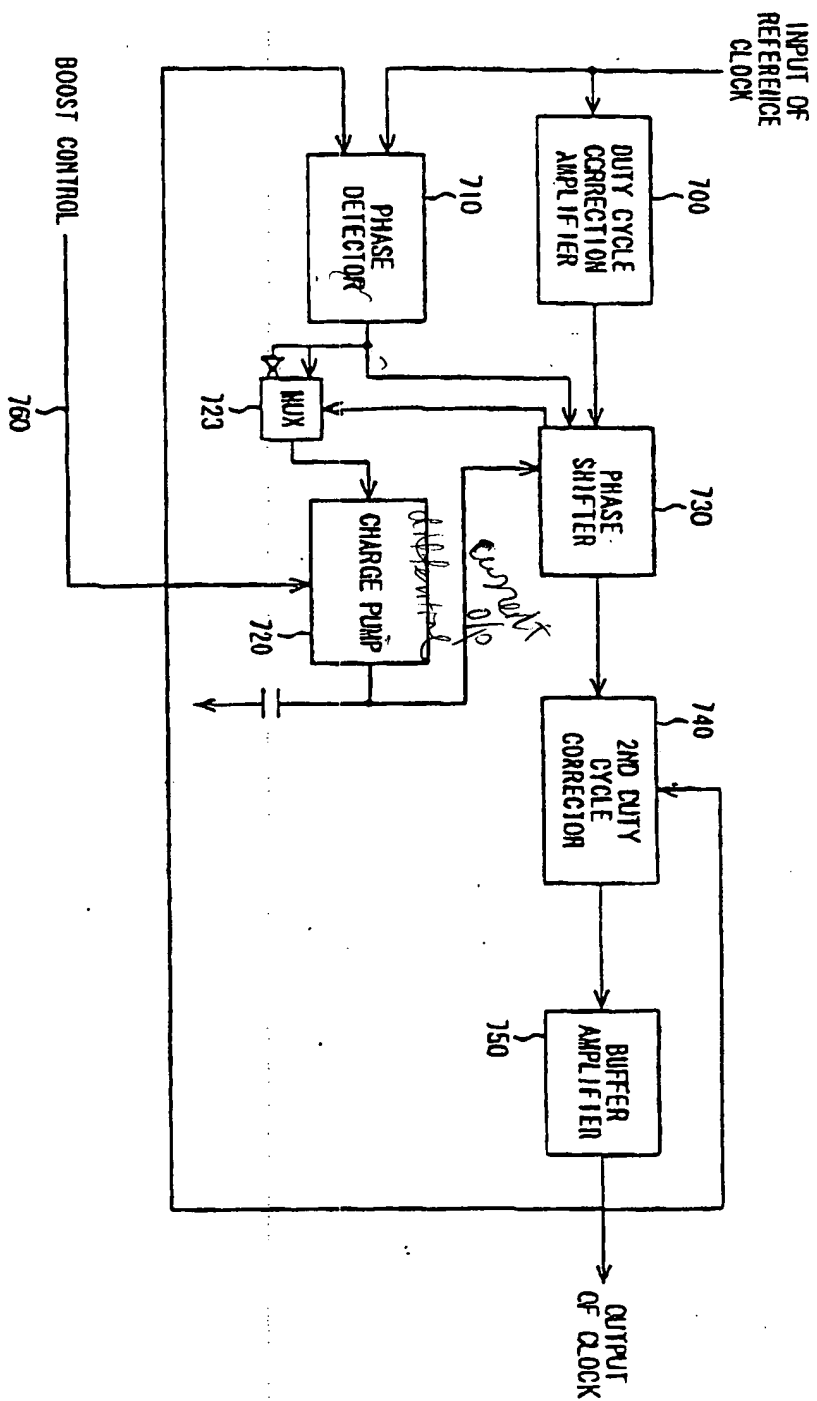
Should be pointer to the outer box that includes 511 & 513.

obj

511 & 542 to the same box

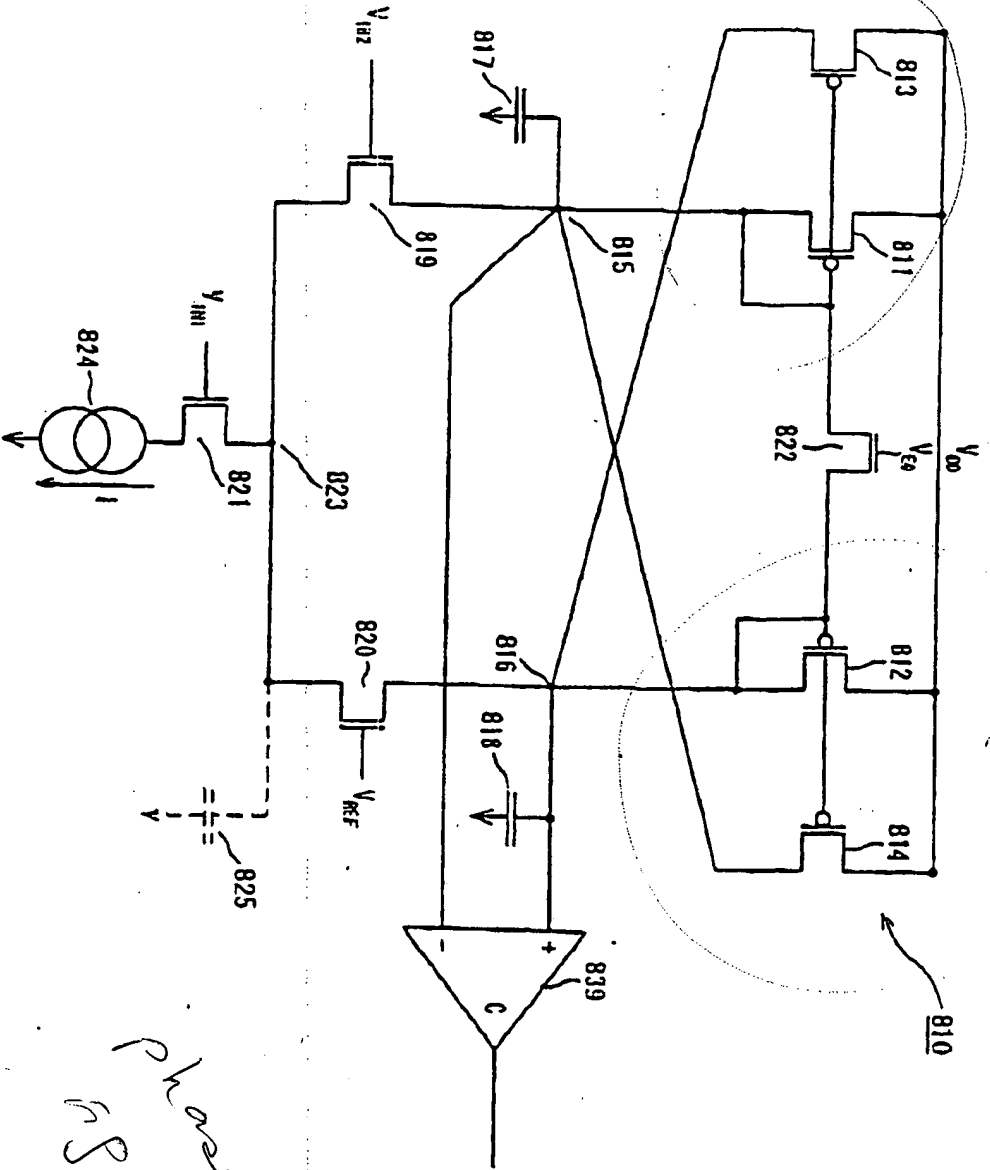
106020-08493360

FIG. 2 PRIOR ART

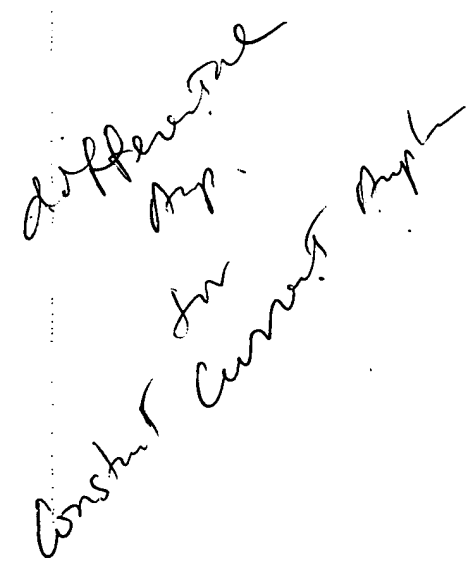


96360  
I prefer 1/4 for  
constant current

At  
Jensen  
phase  
2800

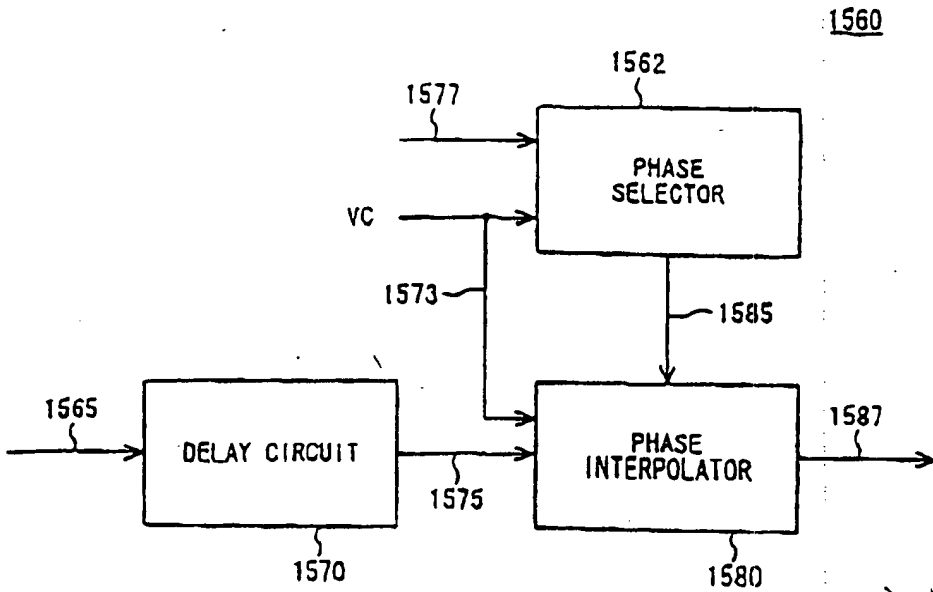


22 July 1964  
D. L. S. 344  
915  
950  
Corr



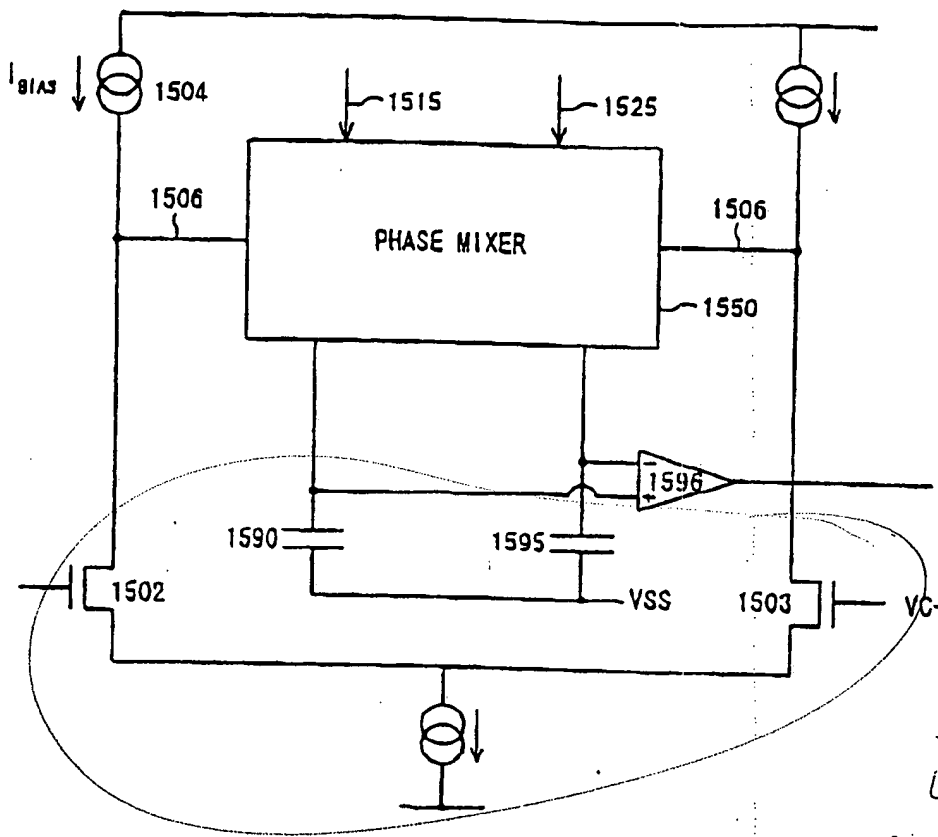
SECRET

FIG. 5 PRIOR ART



phase shifter of Fig. 2

FIG. 6 PRIOR ART



Differential  
Amp.

for

constant current!

phase shift 7 fig 5.

FIG. 7

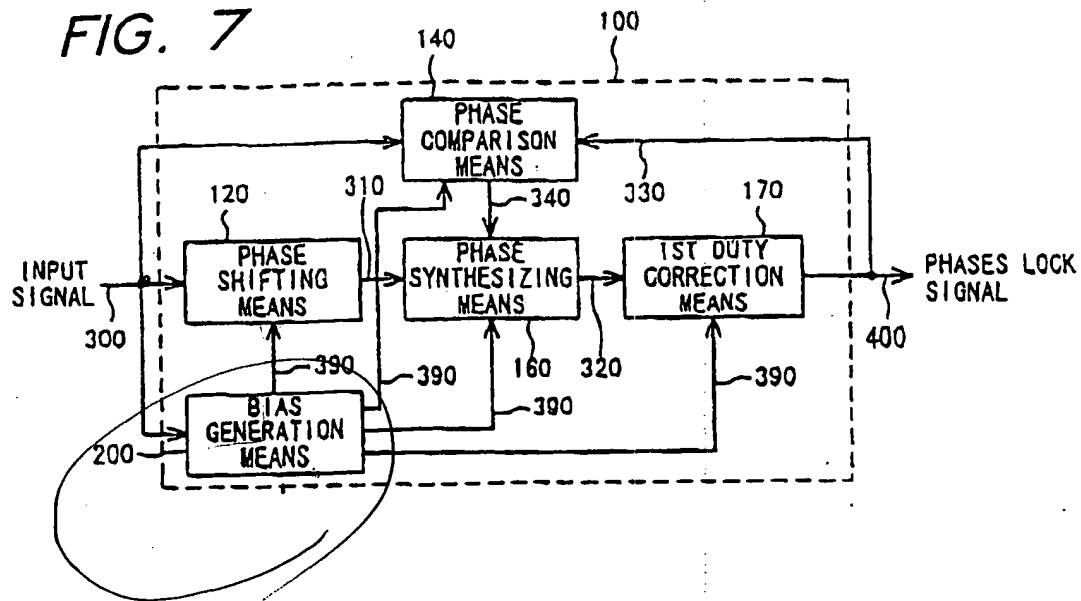
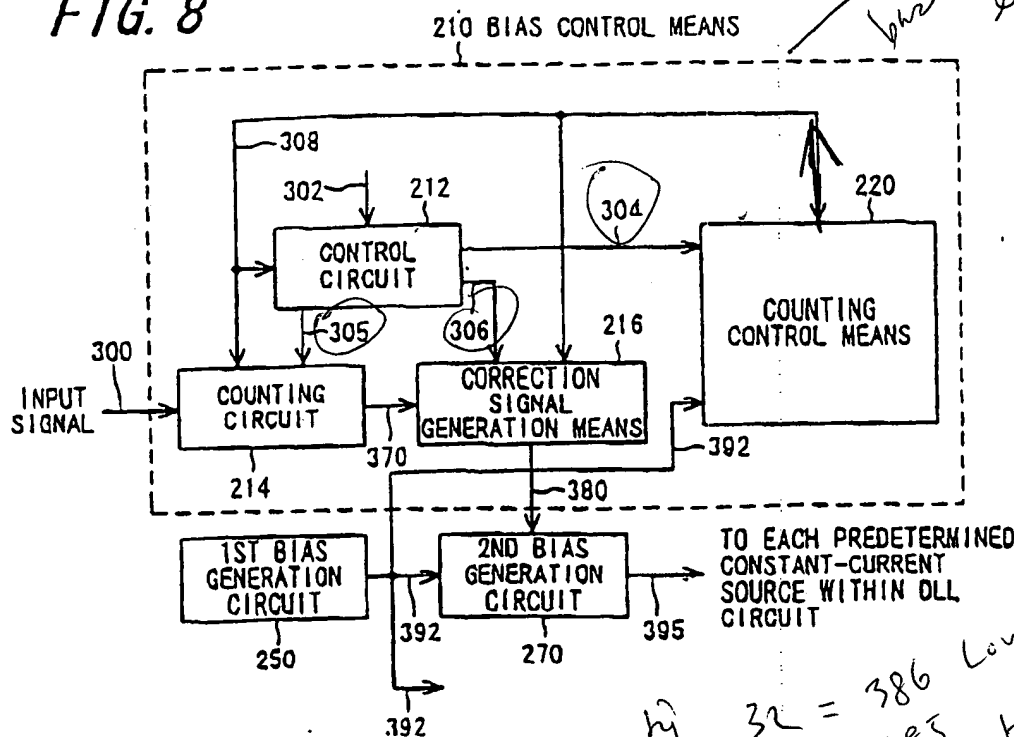


FIG. 7

SECRET


FIG. 8



304-306  
1st counting  
control signal  
base on a  
external  
trigger  
signal  
302

CONTROL MEANS

The diagram consists of two rectangular sections. The top section is labeled 'CONTROL MEANS' and contains a dashed line. The bottom section is labeled 'base' and contains a solid line. A horizontal dashed line separates the two sections. A vertical solid line is on the right side of the 'base' section. A diagonal solid line runs from the top right corner of the 'CONTROL MEANS' section to the bottom right corner of the 'base' section.

ON  395

CONSTANT-CURRENT  
SOURCE WITHIN PLL  
CIRCUIT

32 = 386 Low level  
385 high  
381

Cont'd  
Reset

Counting CKT.

try CLK.

The min. cost = "8".  
 If lower than "8", correct is system  
 make the % 380 correspond to "8".  
 than "8", said "9" correspond to "9".  
 % 380 is "32".

2f lower than 8, ...  
make the % 380 correspond  
2f higher than "8" said "9"  
216 make the % 380 correspond  
out from 214 is "32".  
the frequency  
win

2. The more count, the higher the frequency of power consumption or consumer waste power.

make

- o  $\tau$  f higher than 8
- o 216 make the  $\tau$  380
- o the more count from 214 is

the frequency

$\Rightarrow$  the frequency count, the higher the frequency

$\Rightarrow$   $f \uparrow$  count  $\uparrow$  then, power consumption is  $\uparrow$

$f \downarrow$  count  $\downarrow$  then, power consumption is  $\downarrow$

In this way, do not waste power

or save power using low-freq operation



Constitution

15/5/2019

15-02-18  
PMS 15-02-18  
Cops.

[illegible]

22-575, 9 at p. 13

2023-11-11

W. R. R. R.

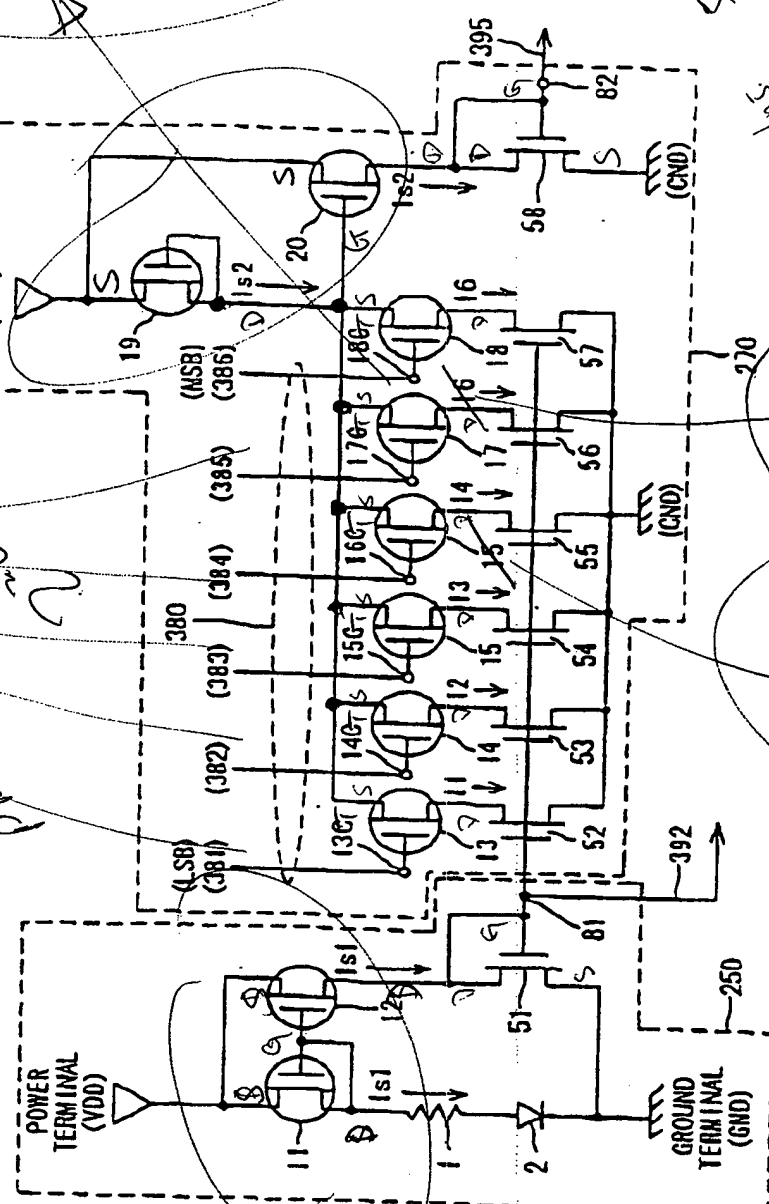
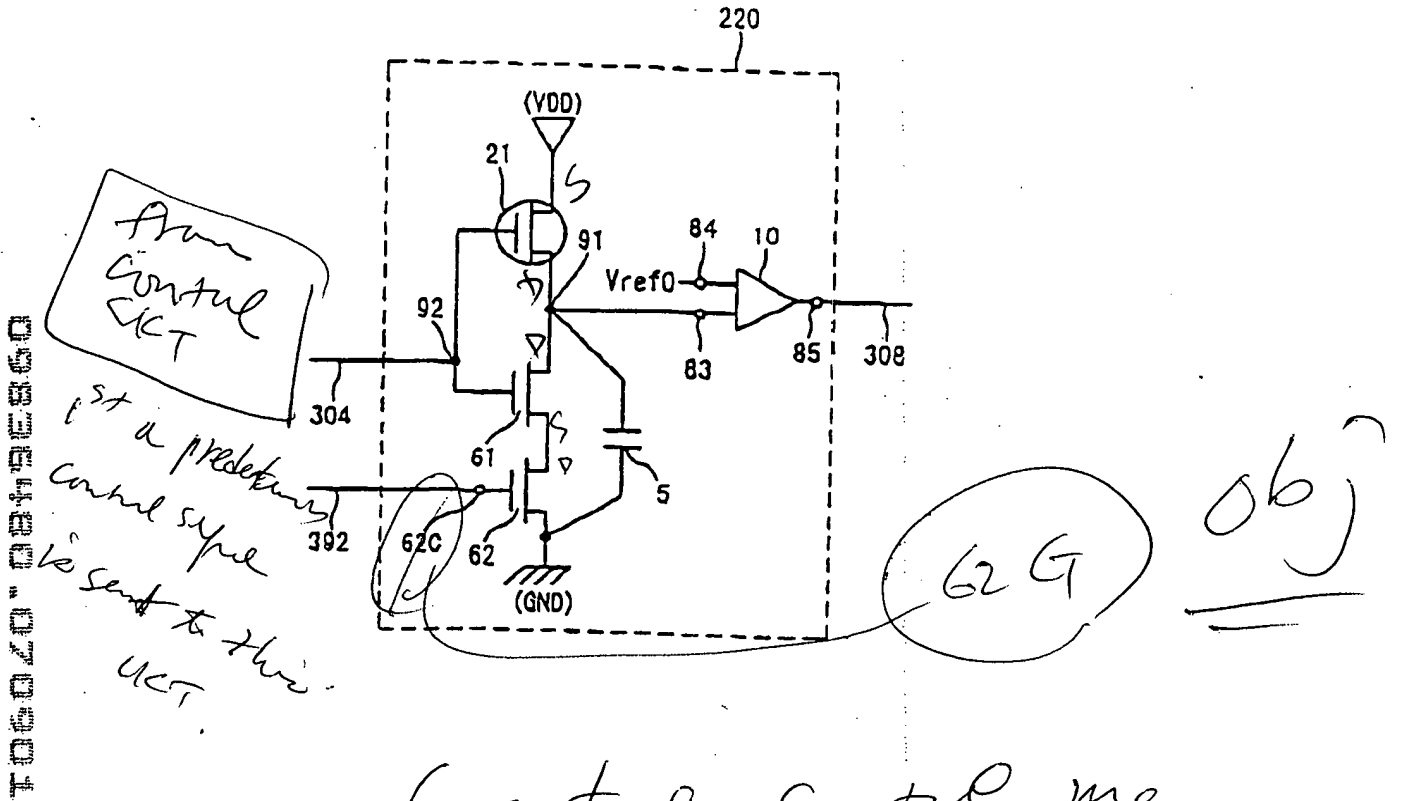


FIG. 9

FIG. 10.



Counter control means

Low level signal

304 will turn 21 (PMOS) on; & NMOS 61 off

Cap. 5 is charged to VDD.

High level signal 304 will turn 21 off & 61 on

Cap 5 is discharged through 61 & 62

FIG. 11A

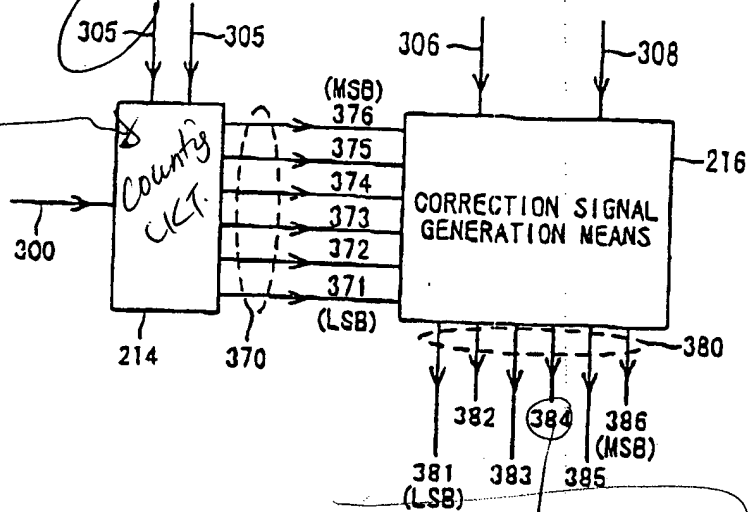
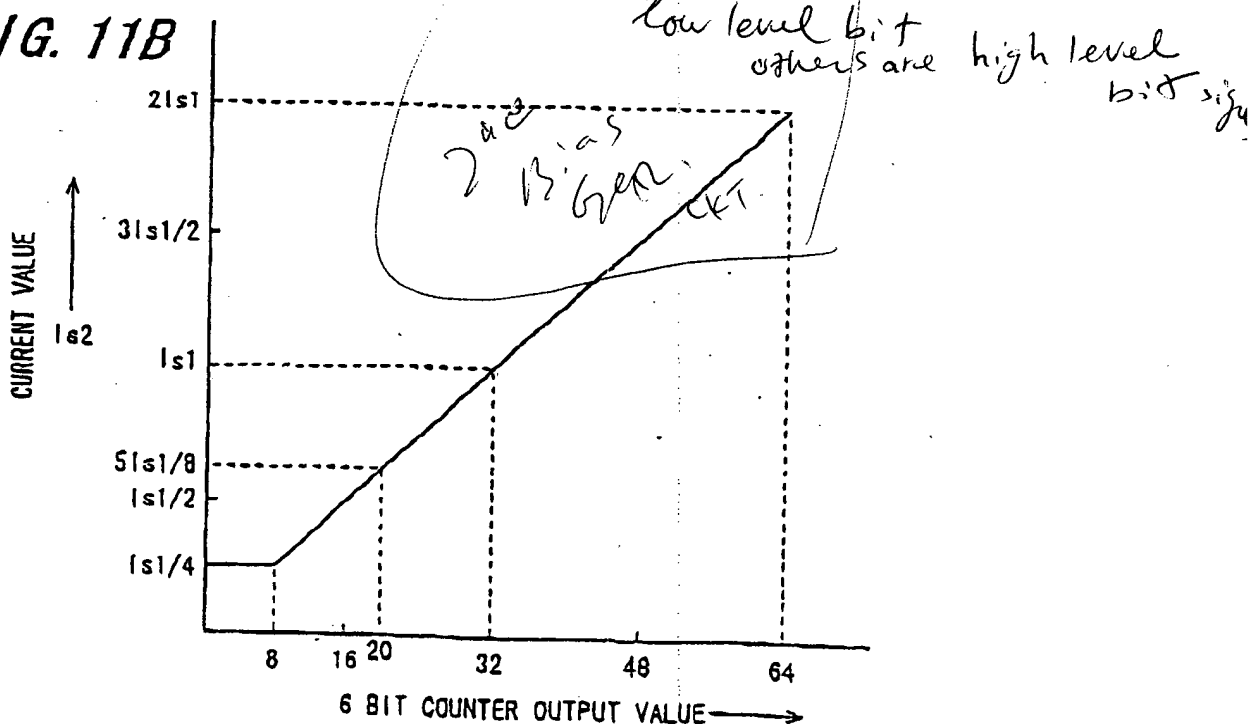


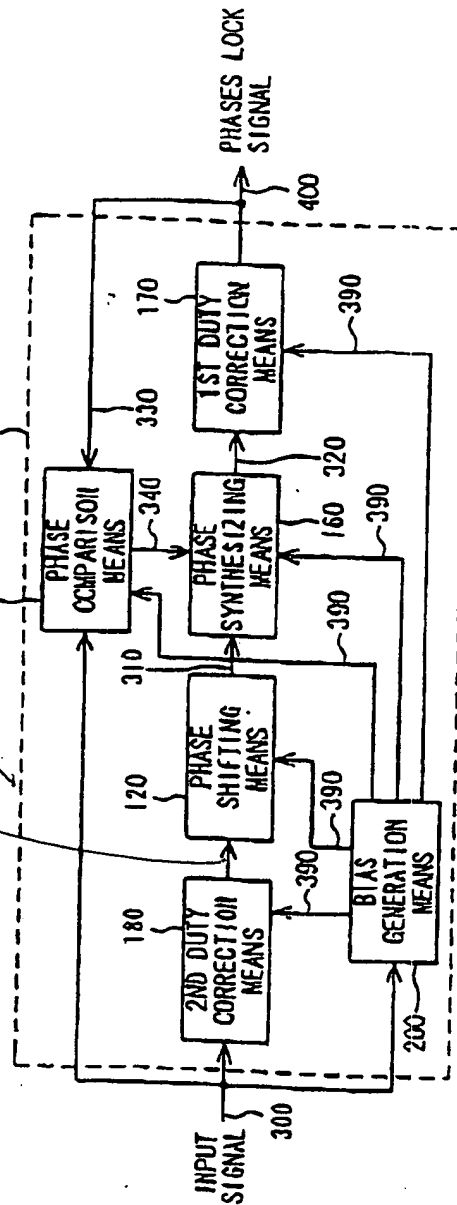
FIG. 11B



86j

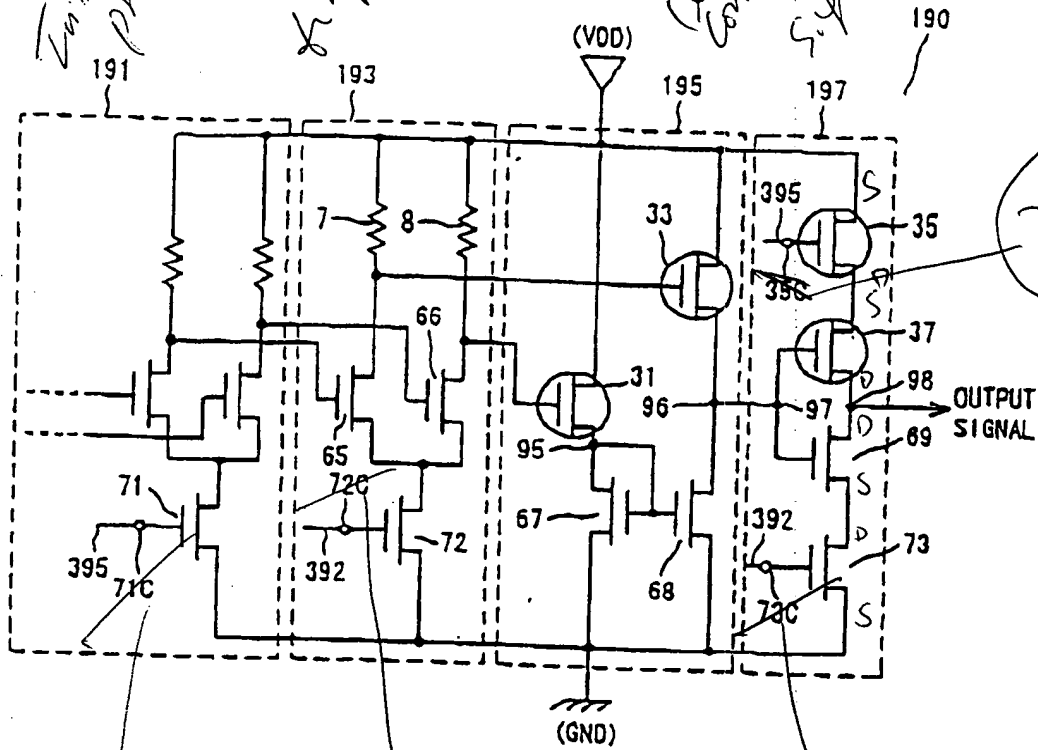
in 3-3-72

FIG. 12



106020-0813E860

FIG. 13



Internal processor  
Level converter  
Signal selection

71G  
Obj

72G

73G

35G  
Obj

FO6070\* 02492B60

FIG. 14

